





## PATENT APPLICATION

In re the Application of

Attn: Box Missing Parts

Satoshi TAKANO

Application No.: 09/819,690

Filed: March 29, 2001

Docket No.: 109107

For:

SEMICONDUCTOR MANUFACTURING METHOD, SUBSTRATE PROCESSING

METHOD, AND SEMICONDUCTOR MANUFACTURING APPARATUS

## SUBMISSION OF DECLARATION

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Submitted herewith is the original signed Declaration of the Inventor[s]. Any specification attached to and referenced in the Declaration is a copy of the specification and any amendments thereto which were filed in the Office in order to obtain a filing date for the application.

Attached is our Check No. 118777 for \$\infty\$\$130.00 \$\subseten\$\$\$\$5.00 (entitlement to small entity status is asserted) for the fee under 37 C.F.R. §1.16(e).

Entry of these documents should complete all of the filing formalities. Accordingly, examination and allowance of this application in due course are respectfully solicited.

The Director is hereby authorized to charge any additional fee (or credit any overpayment) associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this paper are attached.

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Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461

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**AUTHORIZATION**